Adit R. Sachde

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Objective

Experienced Computer Engineering student with an understanding of a variety of embedded systems and infrastructure automation platforms. Strengths include networking, devops, and communications. Additional experience working on educational web platforms (iZen.ai) and collaborating with team members to launch products (Zpparel). Seeking an internship in software engineering starting Fall 2021.

Education

Georgia Institute of Technology | Atlanta, GA *August 2020 – Present*

Bachelor of Science in Computer Engineering, GPA 3.40 / 4.00 Expected Graduation, May 2024

Skills

Programming: C, Rust, Java, Python, JavaScript, Typescript, SQL, Swift, Node.js
Platforms: Linux (Ubuntu, Debian, Nix), Kubernetes, Nomad, GCP, Digital Ocean
Hardware: Raspberry Pi, ARM mbed microcontroller, FPGAs, oscilloscope, logic analyzer
Software: Altera Quartus II, SolidWorks, XCode, GitHub
Professional Organizations: Future Business Leaders of America
Communication: Design proposals, technical reports, instruction manuals, presentations
Languages: English (native), Gujarati (Conversational), Spanish (Conversational)

Experience

iZen.ai | San Jose, CA *May – August 2019*

Software Intern

iZen.ai works in making education accessible to third world countries, using Artificial Intelligence to fill in knowledge gaps.

- Created an automated captioning platform resulted in accessible courses. Allowed for the entire library to be captioned within three months.
- Increased engagement and completion of courses through gamification of the platform.

Zpparel | Johns Creek, GA *August 2018 – May 2019*

Founder

- Worked with artists to bring their designs onto the platform, growing the team to 5 people.
- Developed web store and payment processing platform, allowing for automated order processing.

Projects

Final Digital Design Project *March – April 2021*

Digital Design Lab, ECE2031

Team based project using VHDL and FPGAs.

- Worked with team members to implement a motor encoder and control circuit using FPGAs and VHDL.
- Created a rotary encoder system, converting numeric inputs to letters and outputting them on a dial using assembly.

Relevant Coursework

Digital Design Lab: Analysis, design, and simulation of FPGAs and HDL circuits. Incorporation into prototype computers. Write assembly language programs for the computer. Write lab reports and documentation conforming to technical standards.
Programming HW/SW Systems: Implement high-level language constructs in MIPS assembly. Embedded programming using C on platform based on the MIPS architecture.

Leadership

Programming Club | Johns Creek, GA *August 2018 – May 2020*

President / Vice President

- Joined multiple competitions, such as the Air Force Association's CyberPatriot, the American Computer Science League, and the Lockheed Martin Codequest.
- Coached competitors, resulting in multiple state level wins.
- Made the club more appealing, doubled membership from 10 to 20 members.